

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,943,609 B2
DATED : September 13, 2005
INVENTOR(S) : Zampetti et al.

Page 1 of 1

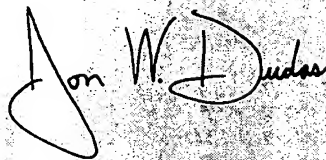
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12,

Line 55, replace "to set the main clock to a main clock phase lock loop normal state" to -- to set the main clock phase lock loop to a main clock normal state --.

Signed and Sealed this

Twenty-seventh Day of December, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a rectangular, textured grey background.

JON W. DUDAS

Director of the United States Patent and Trademark Office